

# APPENDIX A

## TEST EQUIPMENT

Paragraph LIST OF CONTENTS Page No.

1	Introduction	A.1
2	32k RAM Pack Modification	A.1
Fig No.		
A.1	PSU for Test Fixture	A.2
A.2	RS232 Loopback Test Plug	A.3
A.3	Card Tester Circuit Diagram for Slot Tester and Bus Extension Tester	A.4
A.4	Slot Tester Test Head Circuit Diagram	A.5
A.5	Bus Extension Tester Test Head Circuit Diagram	A.6

### 1. INTRODUCTION

- 1.1 Appendix A provides the circuit diagrams for the Z88 test equipment. It also provides information on the modification required to a standard 32k RAM pack in order to transform it into the slot 2 test card.

### 2. 32k RAM PACK MODIFICATION

- 2.1 In order to modify a standard 32k RAM pack to produce a test pack for use in System Test, certain wiring must be connected from the chip to the card connectors as follows:

<u>32k RAM Chip</u>		<u>Card Connector</u>
A12 (pin 2)	wire to	A17 (pin 36)
A13 (pin 26)	wire to	A18 (pin 37)
A14 (pin 1)	wire to	A19 (pin 38)